

Two-way Set Associative Cache - 100

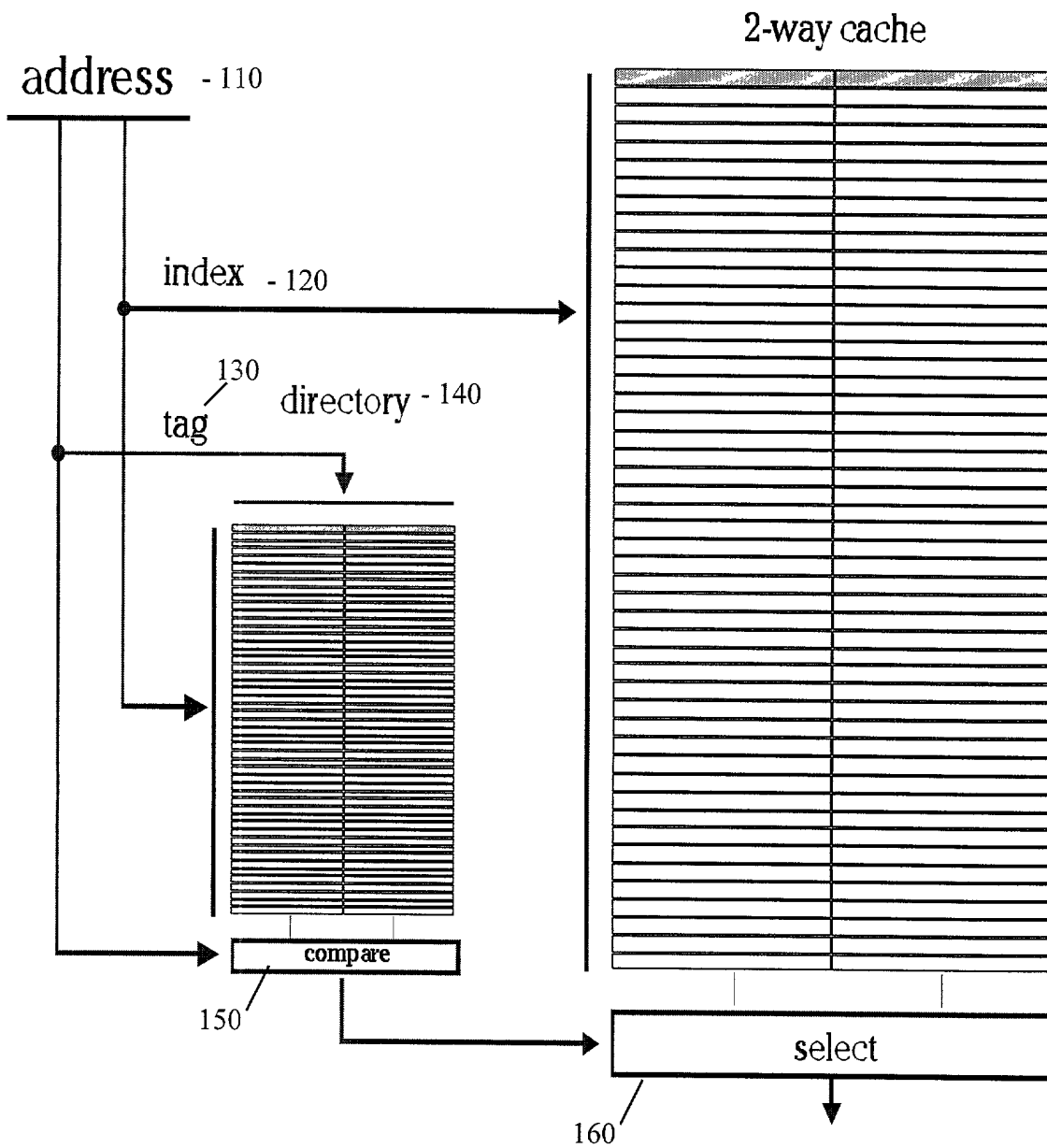


FIG. 1
PRIOR ART

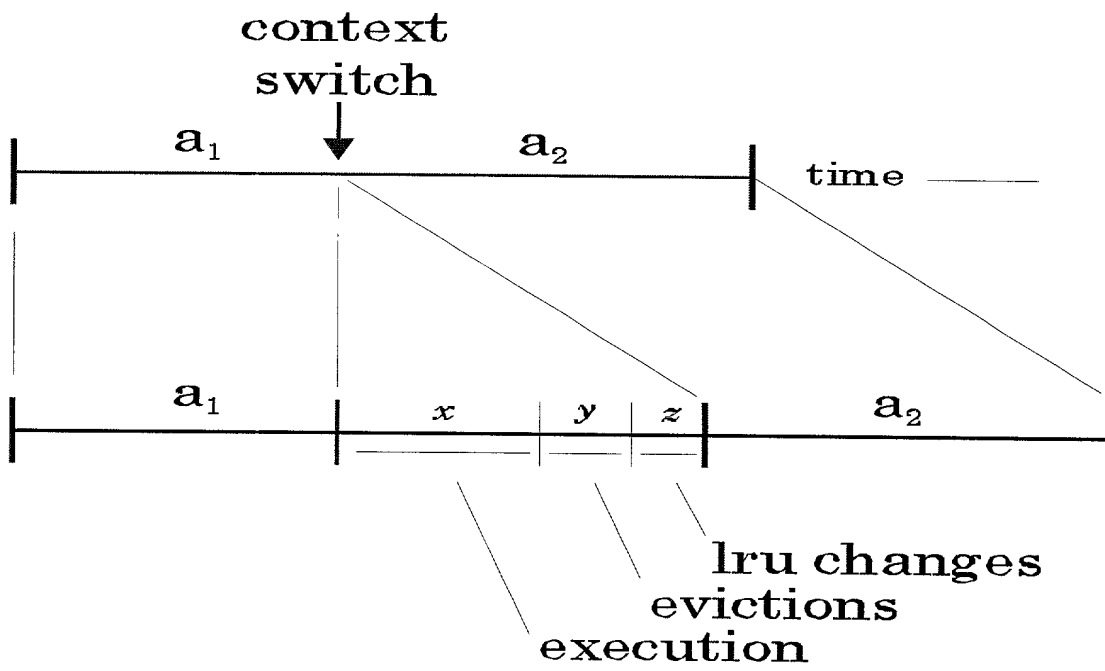


FIG. 2

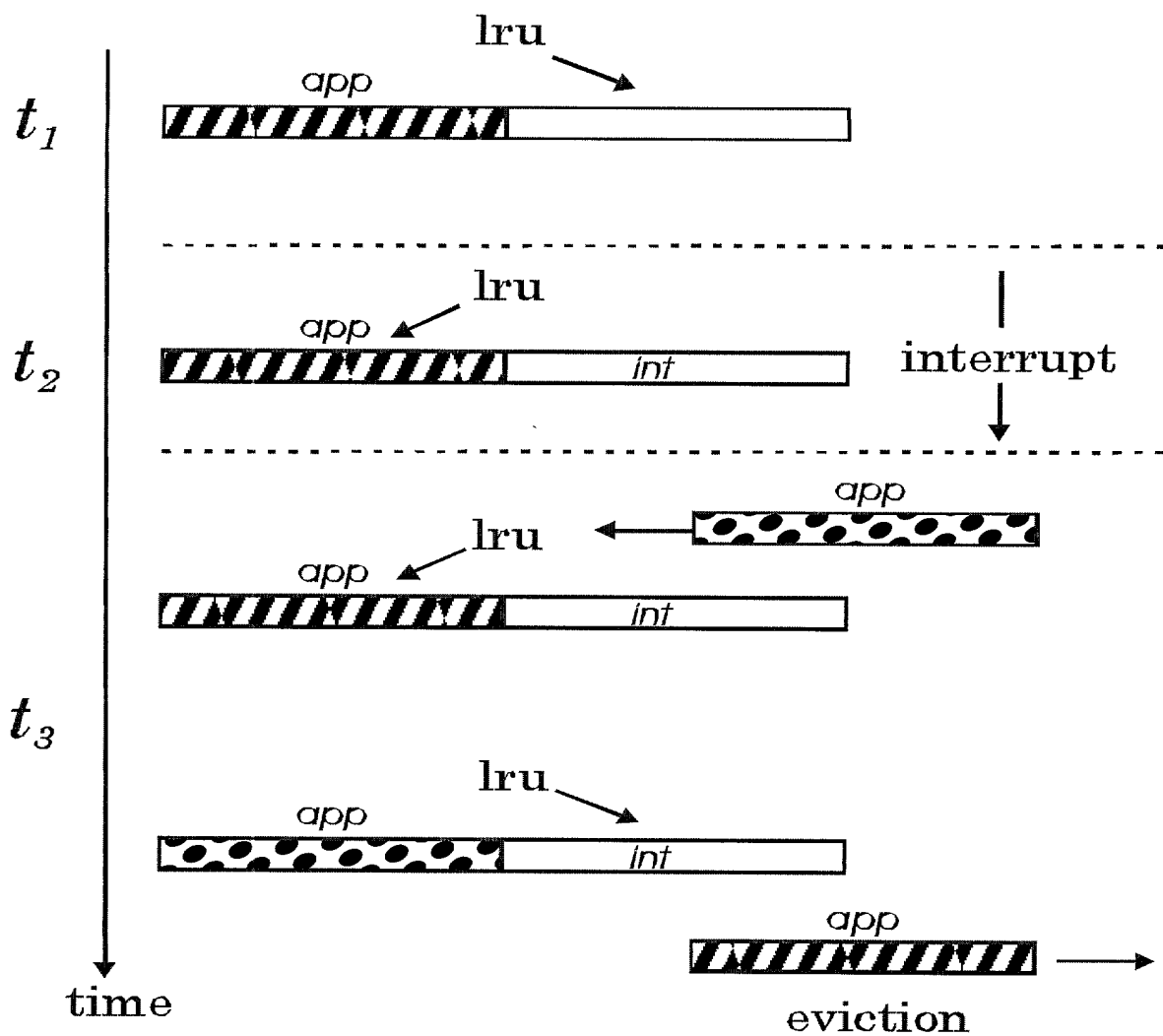


FIG. 3

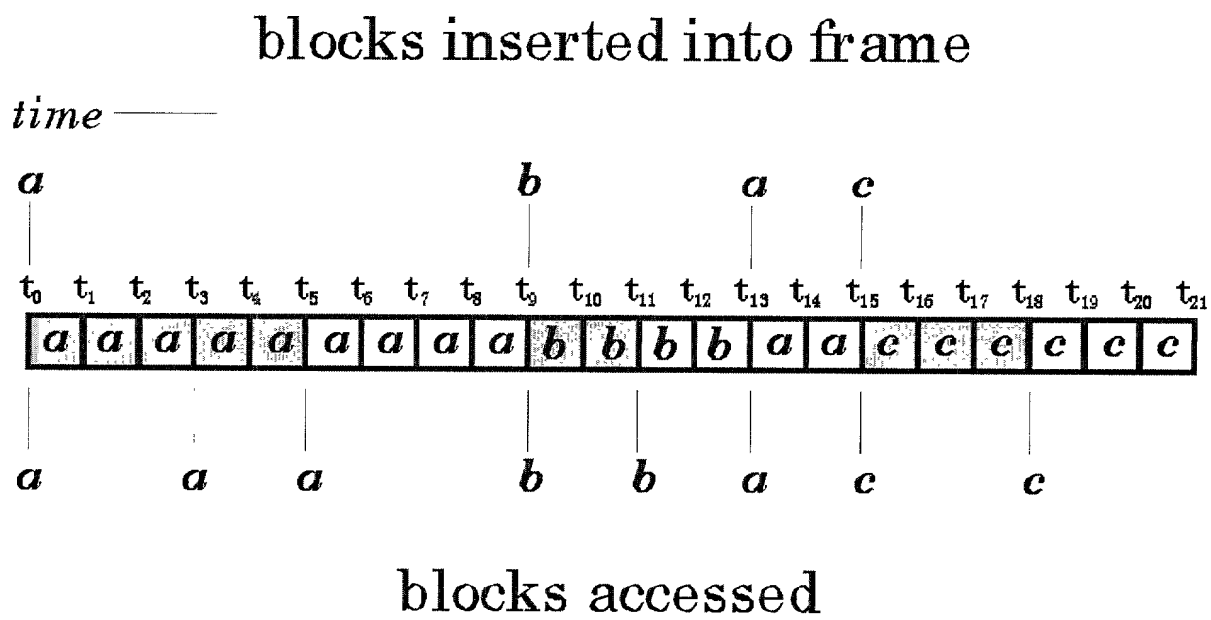


FIG. 4

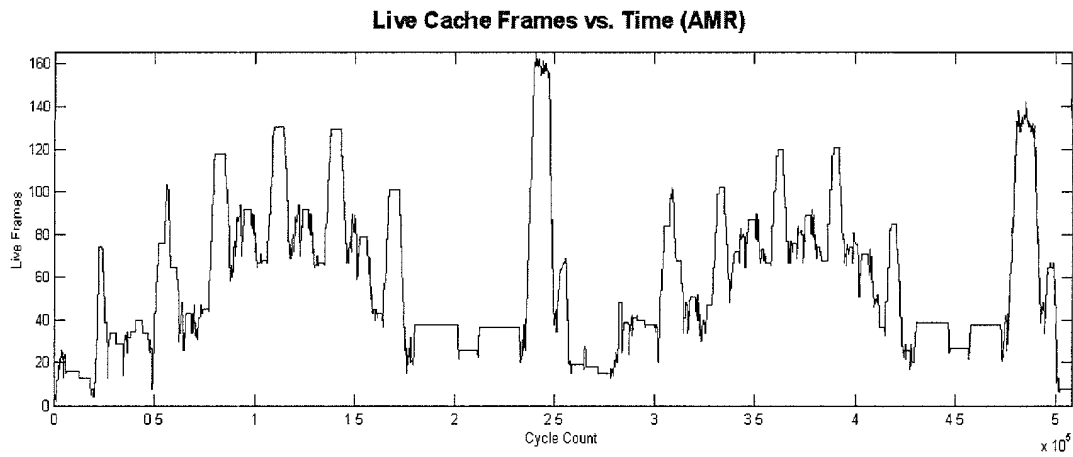


FIG. 5A

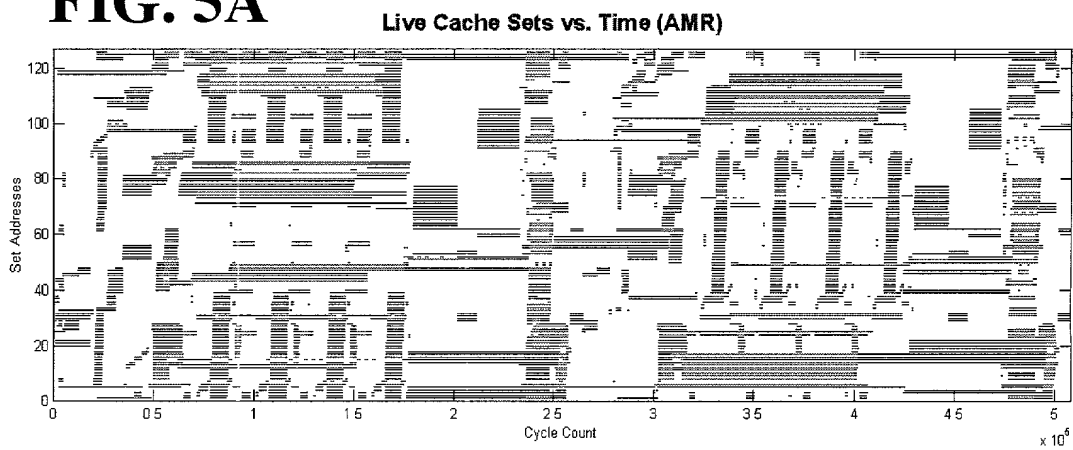


FIG. 5B

Sets containing a live frame at any time during an application's execution.

Sets occupied by an interrupt at any time during its execution.

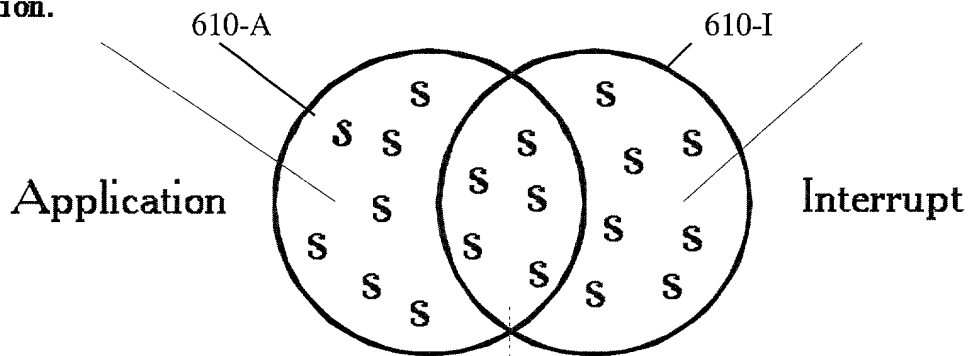


FIG. 6

Sets in the intersection may experience the eviction of a block from a live frame.

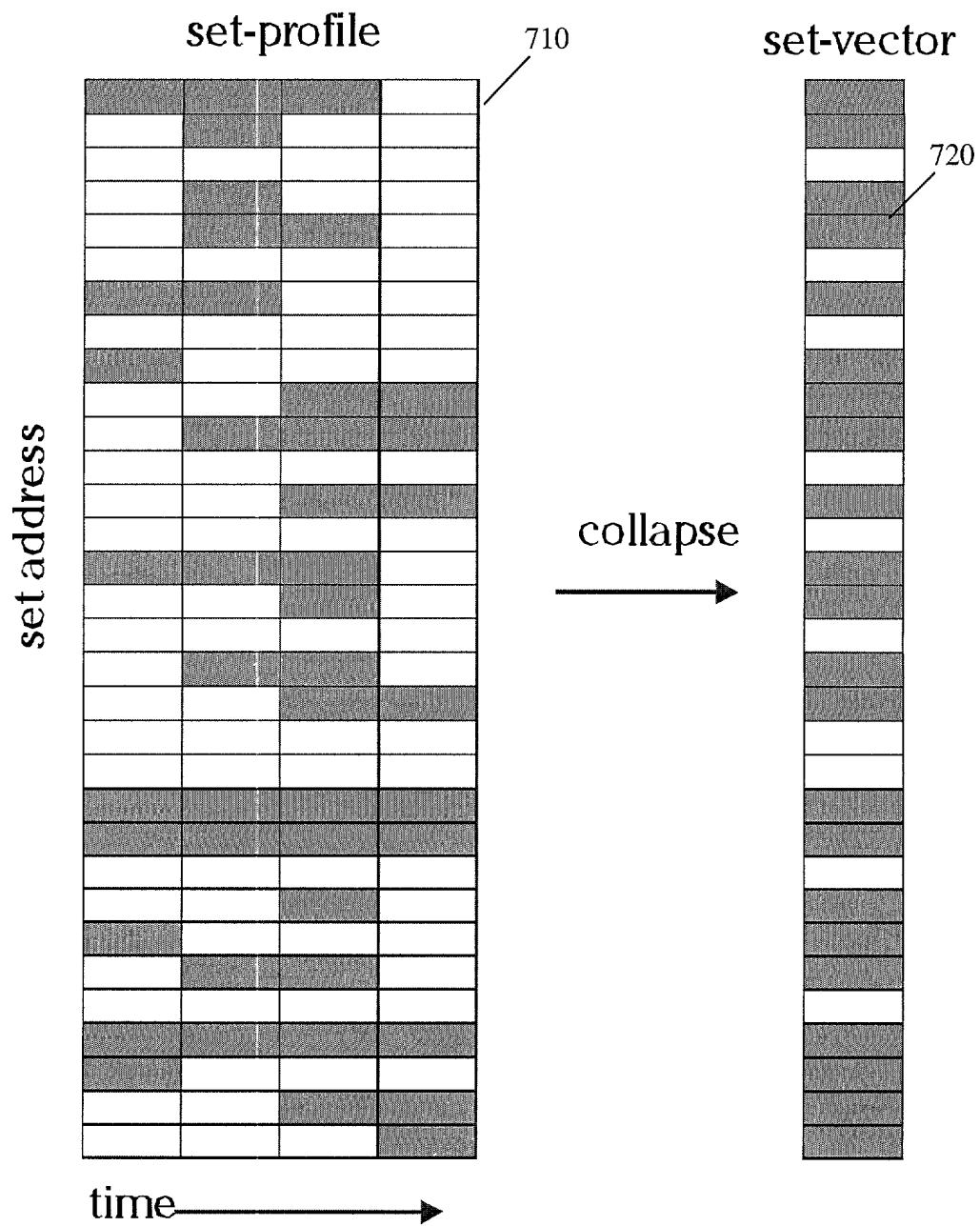


FIG. 7

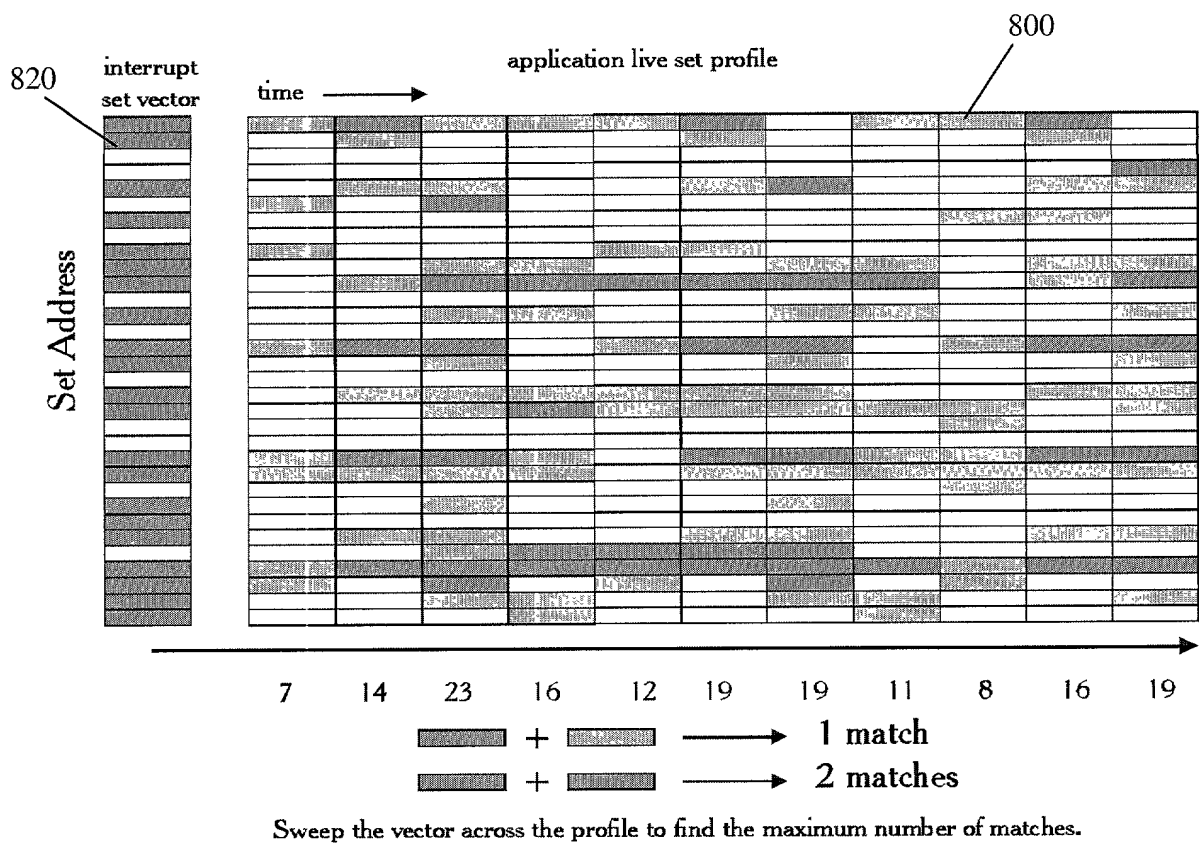


FIG. 8

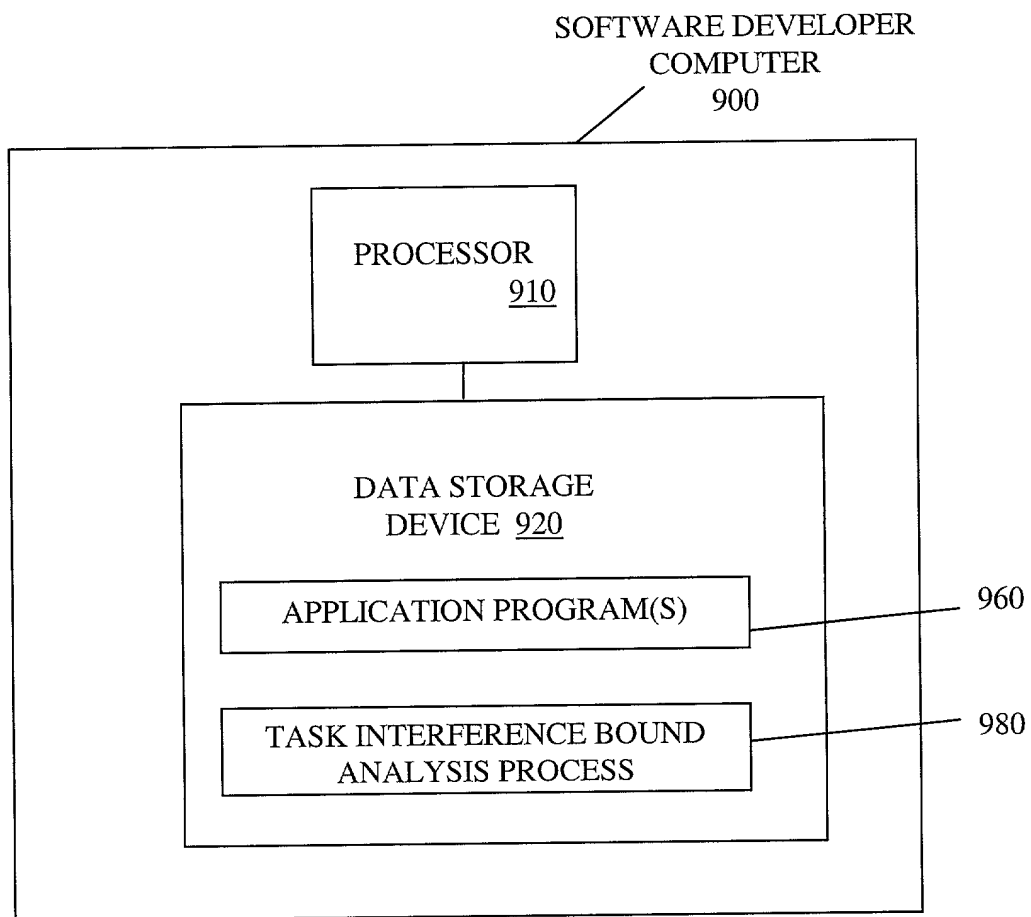


FIG. 9